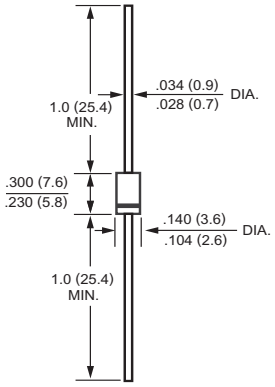
SACxxx

Low Capacitance TRANSZORB Transient Voltage Suppressors

FEATURES

* Plastic package has underwriters laboratory
* Glass passivated chip construction
* 500 watt peak pulse power capability with a 10/1000us waveform,repetition rate (duty cycle):0.01%
* Excellent clamping capability
* Low incremental surge resistance
* Very fast response time
* Ideal for data line applications
* High temperature soldering guaranteed: 265 °C /10 seconds,0.375"(9.5mm) lead length, 5lbs.(2.3kg) tension

DO-15



Dimensions in inches and (millimeters)

Mechanical Data

Case: JEDEC DO-204AC molded plastic body over passivated junction

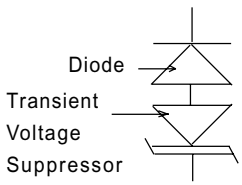
Terminals: Solder plated axial leads,solderable per MIL-STD-750, Method 2026

Polarity: Color band denotes TVS cathode

Mounting position: Any

Weight: 0.015 oz., 0.4g

Schematic



Maximum Ratings and Thermal Characteristics (At T_A = 25°C unless otherwise noted)

RATINGS	SYMBOL	LIMIT	UNITS
Peak pulse power dissipation with a 10/1000uS waveform (note 1)	P_{PPM}	Minimum 500	Watts
Steady state power dissipation at T_L = 75°C lead lengths, .375" (9.5 mm) (NOTES 2)	$P_{M(AV)}$	5.0	Watts
Peak pulse forward surge current with a 10/1000us waveform(fig.3)	I_{FSM}	100	Amps
Operating and storage temperature range	T_J, T_{STG}	-55 to + 150	°C

NOTES : 1. Non-repetitive current pulse, per Fig.3 and derated above T_A = 25°C per Fig.2
2. "Fully ROHS compliant", "100% Sn plating (Pb-free)".

RATING AND CHARACTERISTIC CURVES

Fig. 1 – Peak Pulse Power Rating Curve

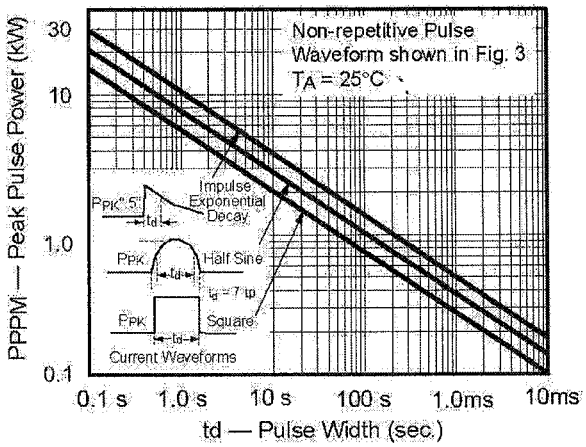


Fig. 2 - Power Derating Curve

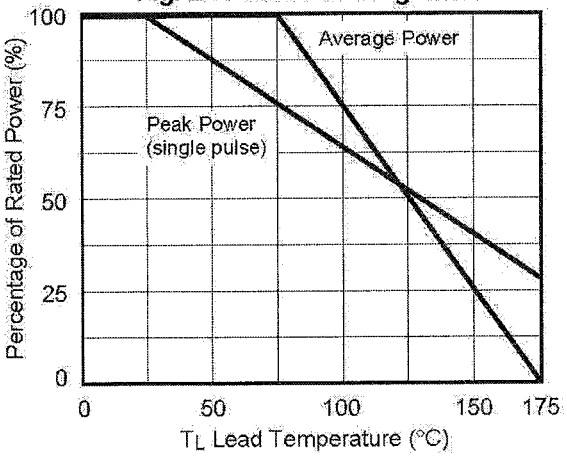


Fig. 3 – Pulse Waveform

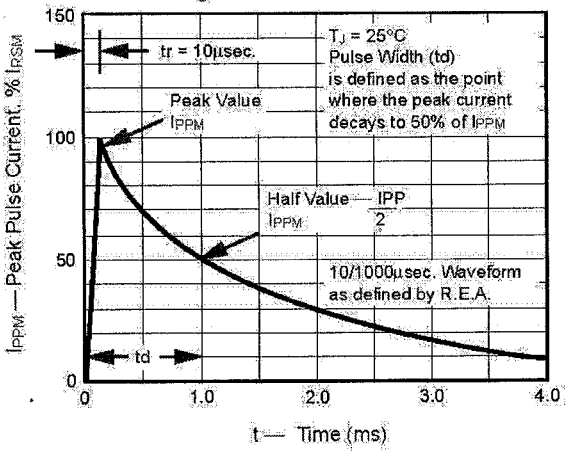
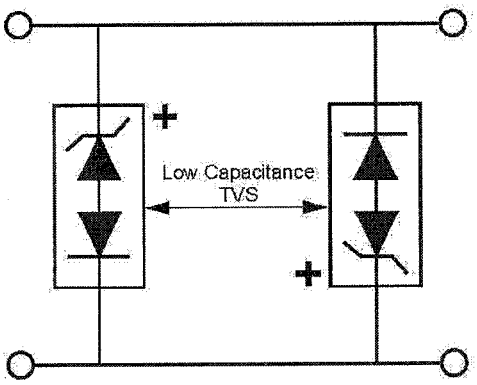


Fig. 4 - AC Line Protection Application



Application Note: Device must be used with two units in parallel, opposite in polarity as shown in circuit for AC signal line protection.

ELECTRICAL CHARACTERISTICS

House No.	Reverse Stand off Voltage V_{WM} * (Volts)	Minimum Breakdown voltage at I_T=1.0mA $V_{(BR)}$ (V)	Maximum Reverse Leakage at V_{WM} I_D (uA)	Maximum Clamping Voltage at I_{PPM}=5.0uA V_C (Volts)	Maximum Peak Pulse Current I_{PPM} (Amps)	Maximum Junction Capacitance at 0 Volts (PF)	Working Inverse Blocking Voltage V_{WB} (V)	Inverse Blocking Leakage Current V_{WB} I_{IB}(mA)	Peak Inverse Blocking Voltage V_{PIB} (V)
SACxxx	5.0	7.60	300	10.0	44	50	75	1.0	100

* Non -repetitive current pulse,per Fig.3 and derated above TA=25 degree per Fig.2